Department of Materials

Electronic and Interface Materials Laboratory

#### Beyond SiO<sub>x</sub> for Hole-Selective Poly-Si Passivating Contacts - The potential of SiN<sub>x</sub> or AlO<sub>x</sub>

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#### Outline

- Poly-Si contacts or 'TOPCon'
- Current limitations with SiO<sub>x</sub>
- Benefits of  $\text{SiN}_{\text{x}}$  and  $\text{AlO}_{\text{x}}$
- Fabrication
- Transport Properties
- Passivation Properties
- Comparison with other work
- Summary

#### **TOPCon Silicon Cells**

Tunnel Oxide Passivated Contact



#### What comes next for TOPCon?



- SiO<sub>x</sub> poly-Si hole contacts
  - Both Sides passivated contacts 
    →Potential for higher efficiency
  - So far less effective than electron contacts<sup>1</sup>  $\times$   $\rightarrow$  Why?

Note: parasitic absorption from poly-Si on the front – not considered today!

<sup>1</sup>R. Basnet *Appl. Phys. Rev.* (2024), 11, 011311

## Limitations of SiO<sub>x</sub>: Part 1

- Hole Transport Properties



• p-type  $SiO_x$  hole contacts rely on **pinhole** conduction

<sup>1</sup>R. Basnet *Appl. Phys. Rev.* (2024), 11, 011311 S. McNab *AIP Conference Proceedings, (2022), 020013* 

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## Limitations of SiO<sub>x</sub>: Part 2

- P-type Passivation Properties

- Boron is more soluble in  $SiO_x$  than Phosphorus:
  - Deeper diffusion more Auger recombination
  - Boron in SiOx introduces defects higher D<sub>it</sub>
- $\rightarrow$  More recombination







poly-Si

SiO<sub>2</sub> n⁺ poly-Si Rear SiN<sub>\*</sub>

n or p-type Si

# The Benefits of SiN<sub>x</sub> and AlO<sub>x</sub>: Part 1

XPS VBO measurements



#### **Tunnelling current Calculations**



<sup>2</sup>S. McNab AIP Conference Proceedings, (2022), 020013

## The Benefits of SiN<sub>x</sub> and AlO<sub>x</sub>: Part 2

- Boron diffusion profile

#### **ECV Doping Profile**



- $SiN_x$  blocks diffusion
- SiO<sub>x</sub> and AlO<sub>x</sub> deep boron diffusion

<sup>1</sup> S. McNab, *Submitted* 

#### The Benefits of SiNx and AlOx: Part 3 Field effect passivation?

SiOx has very little charge AlOx has a negative charge SiNx has a positive charge



#### Why is this important?

<sup>1</sup>A. Cuevas, IEEE 42nd Photovoltaic Specialist Conference, (2015)

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#### The Benefits of SiN<sub>x</sub> and AlO<sub>x</sub>: Part 3 Field effect passivation?



#### The Benefits of SiNx and AlOx: Part 3 Field effect passivation?



<sup>&</sup>lt;sup>1</sup>A. Cuevas, *IEEE 42nd Photovoltaic Specialist Conference*, (2015) <sup>2</sup>S. McNab, *PhD Thesis*, *University of Oxford*, (2023)

## $SiN_x$ or $AIO_x$ Poly-Si Contacts



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#### **Fabrication Process**



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#### Dielectric layers for Poly-Si deposition



- Why RCA2 SiO<sub>x</sub>?
  - Reasonable chemical passivation
  - Very thin  $\rightarrow$  limited effect on the tunnelling conduction

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#### **Transport Properties**





<sup>1</sup> S. McNab, *Submitted* 

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### **Understanding Transport Mechanisms**

• Temperature Dependent IV measurements



<sup>2</sup> S. McNab, *IEEE Journal of Photovoltaics*, (2022), 1-11

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### **Understanding Transport Mechanisms**



#### • Batch 1a: Adjust anneal Temperature



- 800 °C anneal shows a purely tunnelling fit
- >850 °C has a high pinhole density
- Pinholes are not required for  $\rho_c < 100 \text{ m}\Omega \cdot \text{cm}^2$
- But they might form anyway!

<sup>1</sup> S. McNab, *Submitted* 

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#### Passivation





- Low iV<sub>OC</sub> compared to SiO<sub>x</sub> control X
  AIO too low to even measure!
- $AIO_x$  too low to even measure!
- Why?

<sup>1</sup> S. McNab, *Submitted* 

#### **Characterising Passivation: Uniformity**



Severe inhomogeneity issues – but some areas with high passivation

<sup>1</sup> S. McNab, *Submitted* 

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- Thick dielectrics
- How do the properties of <2nm dielectrics compare
- Difficult to measure due to high conductivity

<sup>1</sup>A. Cuevas, IEEE 42nd Photovoltaic Specialist Conference, (2015)

- Surface Photovoltage
- No direct contact to the dielectric
  - Prevents conduction





• Effect of D<sub>it</sub> and Q<sub>f</sub> difficult to deconvolute

<sup>1</sup> S. McNab IEEE Journal of Photovoltaics, (2022), 1-11

#### Capacitance-Voltage Measurements



- CV is commonly used for thick dielectrics
  - Q<sub>f</sub> shifts curve left/right
  - D<sub>it</sub> changes the slope
- Sensitive to interface charge
- High conductivity prevents an accurate CV measurement



<sup>1</sup> S. McNab, *Submitted* 



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### **Improved Passivation**



#### Takeaways from Batch 1

- Tunnelling contacts are possible
- SiN<sub>x</sub> blocks boron
- RCA2+AlO<sub>x</sub> has high -ve charge
- High pinhole density X
- Uniformity issues X
- Used anneal optimised for SiO<sub>x</sub>

#### For Batch 2

- Varied anneal conditions
- Improved RCA2 processing and wafer handling
- Focus on most promising interlayers



Batch 2 after hydrogenation



<sup>1</sup> S. McNab, *Submitted* 

RCA2 + SiN<sub>x</sub>

RCA2+AIO,

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#### Comparison to other work



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### Summary

- Potential benefits of PECVD  $\text{SiN}_{\rm x}$  and ALD  $\text{AlO}_{\rm x}$  as hole selective poly-Si tunnelling contacts
- Developed Methods for Characterising  $\mathbf{Q}_{\mathrm{f}}$  and  $\mathbf{D}_{\mathrm{it}}$  of 2nm dielectrics
- Significant improvement in 2 batches:
  - SiN<sub>x</sub> iV<sub>OC</sub> 668→699 mV
  - AlO<sub>x</sub> shows promise before poly-Si







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#### XPS data

- SiNx stoichiometry low N concentration, close to SiOx
- AIOx significant increase in AI concentration





